

**Amendments to the Specification:**

Please amend the specification as follows:

**Please replace paragraph [0032] with the following rewritten paragraph:**

The present invention improves the file system management by having the memory card controller ~~120~~ 130, which is included on the memory card 120 in the first embodiment, perform the FAT, FDC descriptor and Root Directory management and in the second embodiment perform the FAT management instead of having the host device 100 perform these functions. In that regard, the memory card controller 130 provides the command interface to emulate, in the first embodiment the file management module and in the second embodiment the FAT module, that is implemented in software on the host device of conventional host devices.

**Please replace paragraph [0036] with the following rewritten paragraph:**

Figure 1 shows a host device 100 outputting an access command 110 to write a data file to a memory card 120. Referring now to Figure 2, the memory card controller 130 receives the access command output from the host device 100, and obtains the FAT 500 of the memory card ~~120~~ from the memory card storage memory 140. As a first alternative to the FAT retrieval described above, the memory card controller 130 accesses the FAT 500 at power up and stores the FAT 500 and file system structures in memory 210, whereby the memory card controller 130 does not have to obtain the FAT 500 from the memory card storage memory 140 again in response to an access command that it receives. As a second alternative to the FAT retrieval described above, the memory card controller 130 determines the location of the FAT 500 in the memory card storage memory 140, whereby the memory card controller 130 only keeps a portion of the FAT 500 that is to be updated in memory 210.

**Please replace paragraph [0043] with the following rewritten paragraph:**

In the first embodiment, the processor ~~210~~ 220 responds to the same set of access commands that are output by a conventional host device, and the processor ~~210~~ 220 maintains and updates the FAT, FDC and Root Directory of the memory card 120 while fulfilling the access commands. Those access commands include: 1) write of N sectors of data to a memory card starting at location A, 2) read of M sectors of data from a memory card starting at location B, 3) free up Z sectors of contiguous memory space on the memory card starting at location C. The access commands output from the host device also include: 1) listing of files on the memory card, 2) moving of files on the memory card, 3) deleting of files on the memory card, and 4) copying of files from the memory card.

**Please replace paragraph [0045] with the following rewritten paragraph:**

In a third step 330, the processor ~~210~~ 220 of the memory card controller 130 determines if the memory card 120 has sufficient available storage space to fulfill the file write request made by the host device 100. In doing so, the memory card controller 130 performs functions that were performed by the conventional host device 100 in determining whether sufficient memory space exists on the memory card 120.

**Please replace paragraph [0045] with the following rewritten paragraph:**

In the ~~second~~ first embodiment, all of the FAT management and FAT updating is done by the memory card controller 130, and thus the host device 100 only has to output the data file to be written to the memory card 120, and the host device 100 is not provided with any FAT information as to the memory locations (e.g., sectors or clusters) of the memory card 120 on which the data file is being written to by the memory card controller 130. The memory card controller 130 exclusively maintains that information.